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PATENT  
8017-1053

IN THE U.S. PATENT AND TRADEMARK OFFICE

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Technology Center 2100

In re application of

Yusuke MATSUSHIMA

Conf. 5194

Application No. 10/022,428

Group 2133

Filed December 20, 2001

Examiner Albert Decady

MASTER-SLAVE-TYPE SCANNING  
FLIP-FLOP CIRCUIT FOR HIGH-SPEED  
OPERATION WITH REDUCED LOAD CAPACITY  
OF CLOCK CONTROLLER

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed Form PTO-1449.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in an Official Action. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion enclosed in

a wavy line. An English translation of the enclosed portion is also attached hereto.

Respectfully submitted,

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